

FIG.2

(b)	260,	- 79,	Ο,	-8,	- 2,	- 3,	-4,	1,	
	520,								
(d)	16,	12,	14,	14,	18,	24,	49,	72,	
DECIDED RESULT OF COMPARISON	1,	1,	Ο,	1,	Ο,	Ο,	Ο,	Ο,	•••

FIG.3

(e) PREDICTING REGISTER

1	1	1	0	0	0	0	0
1	1	0	0	0	0	0	0
0	1	0	0	0	0	0	0
1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0
	1 0 0 0	1 0 0 0 0 0 0 0	0 1 0 1 0 0 0 0 0 0 0 0	1 1 0 0 0 1 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 0 1 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0	1 1 0 0 0 0 0 1 0 0 0 0 1 0 0 0 0 0 0 0 0	1 1 0 0 0 0 0 0 1 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0

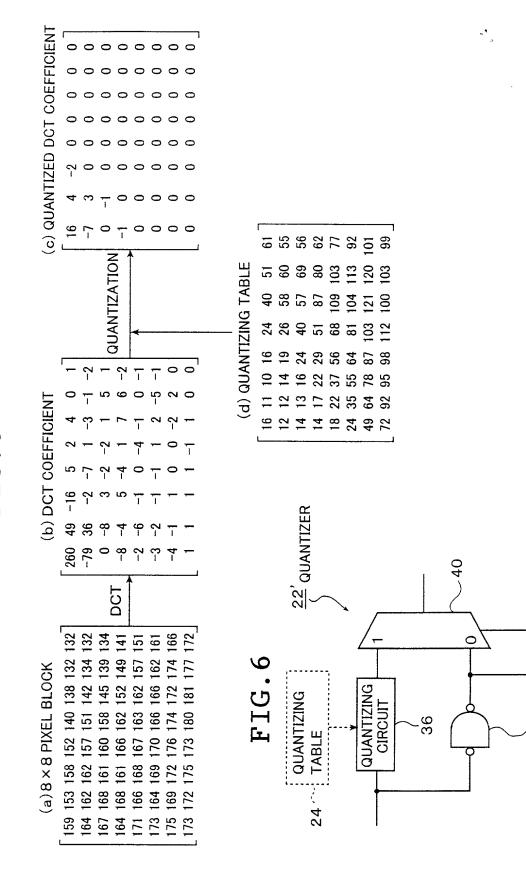
FIG.4

	O -RUN LENGTH	(b)	(c)
1	0	260	16
2	0	49	4
3	0	- 79	-7
4	1	36	3
5	0	- 16	- 2
6	2	- 8	- 1
7	o	- 8	- 1
8	EOB		

QUANTIZER QUANTIZING TABLE 22 24 QUANTIZING TABLE QUANTIZER (O) DATA QUANTIZING DEVICE 42 RAM 20 COMPARATOR * RAM (P) 20 28, **PRIOR ART** 1-D DCT FIG. 5 FIG. 7 18 SHIFT CIRCUIT 26 RAM 16 1-D DCT 8 1-D DCT 14 RAM (a) 1-D DCT 14

(P)

FIG. 8



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